

Description

The TDL501 series combine an AlGaAs infrared emitting diode as the emitter which is optically coupled to a silicon high speed photo transistor in a plastic LSOP6 package.

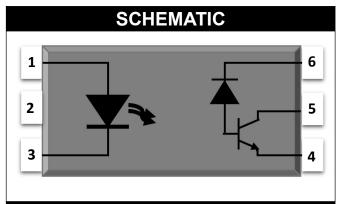
With the robust coplanar double mold structure, TDL501 series provide the most stable isolation feature.

Features

- High isolation 5000 VRMS
- DC input with high speed transistor
- Operating temperature range 40 °C to 100 °C
- REACH compliance
- Halogen free
- MSL class 1
- Regulatory Approvals
 - UL UL1577
 - VDE EN60747-5-5(VDE0884-5)
 - CQC GB4943.1, GB8898
 - cUL- CSA Component Acceptance
 Service Notice No. 5A

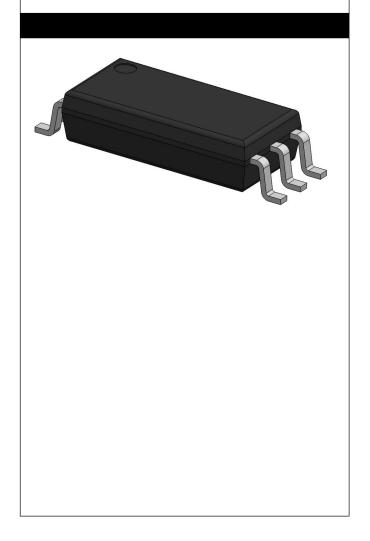
Applications

- Line receivers
- Telecommunication equipment
- Out interface to CMOS-LSTTL-TTL
- Wide bandwidth analog coupling
- Pulse transformer replacement
- Computer-peripheral interface



PIN DEFINITION

- 1. Anode
- 6.VCC
- 2. No Connection
- **5.VO**
- 3. Cathode
- 4.GND





ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	VALUE	UNIT	Note			
	INPUT						
Forward Current	l _F	25	mA				
Peak Forward Current	I _{FP}	50	mA	1			
Peak Transient Current	I _{F(trans)}	1	Α	2			
Reverse Voltage	V_R	5	V				
Input Power Dissipation	Pı	100	mW				
	OUTPUT						
Supply Voltage	V _{CC}	-0.5~30	V				
Output Voltage	Vo	-0.5~20	V				
Output Current	lo	8	mA				
Peak Output Current	Io	16	mA				
Output Power Dissipation	Po	100	mW				
COMMON							
Total Power Dissipation	Ptot	200	mW				
Isolation Voltage	Viso	500	Vrms	3			
Operating Temperature	Topr	-40~100	°C				
Storage Temperature	Tstg	-55~125	°C				
Soldering Temperature	Tsol	260	°C	4			

Note 1. 50% duty, 1ms P.W

Note 2. ≤1μs P.W,300pps

Note 3. AC For 1 Minute, R.H. = $40 \sim 60\%$

Note 4. For 10 seconds



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ELECTI	RICAL OF	PTICA	L CHA	RAC	TERIS	STICS at Ta=25°C	
PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION	NOTE
INPUT							
Forward Voltage	V _F	-	1.35	1.8	V	I _F =16mA	
Reverse Current	I _R	_	-	10	μA	V _R =5V	
Input Capacitance	Cin	-	60	-	pF	V=0, f=1MHz	
			OUT	PUT			
High Level Supply Current	Іссн	-	0.08	1	μA	I _F =0mA, V _O =Open, V _{CC} =30V, Ta=25°C	
Low Level Supply Current	I _{CCL}	-	600	-	μA	I _F =16mA, V _O =Open, V _{CC} =30V	
Logic High Output Current		-	0.001	0.5	μA	I_F =0mA, V_O = V_{CC} =5.5 V , Ta =25 $^{\circ}$ C	
	Іон	-	0.01	1	μA	I _F =0mA, V _O =V _{CC} =15V, Ta=25°C	
		_	-	50	μA	I _F =0mA, V _O =V _{CC} =15V	
TRANSFER	CHARACT	ERISTI	CS(at T	a=0 to	70°C ,	unless specified otherwise)	
Current Transfer CTR Ratio	CTD	20	60	100	%	I_F = 16mA , V_O = 0.4V, V_{CC} =4.5V, Ta=25°C	
	15	_	-	70	$I_F = 16mA , V_O = 0.5V,$ $V_{CC}=4.5V$		
Logic Low Output Voltage	V	-	0.08	0.4	V	$I_F = 16\text{mA}, I_O = 3\text{mA},$ $V_{CC}=4.5\text{V}, Ta=25^{\circ}\text{C}$	
	V _{OL}	-	-	0.5	V	$I_F = 16mA$, $I_O = 2.4mA$, $V_{CC}=4.5V$	
Isolation Resistance	Riso	10^12	10^14	-	Ω	DC500V, 40 ~ 60% R.H.	
Floating Capacitance	C _{IO}	_	0.3	-	рF	V=0, f=1MHz	



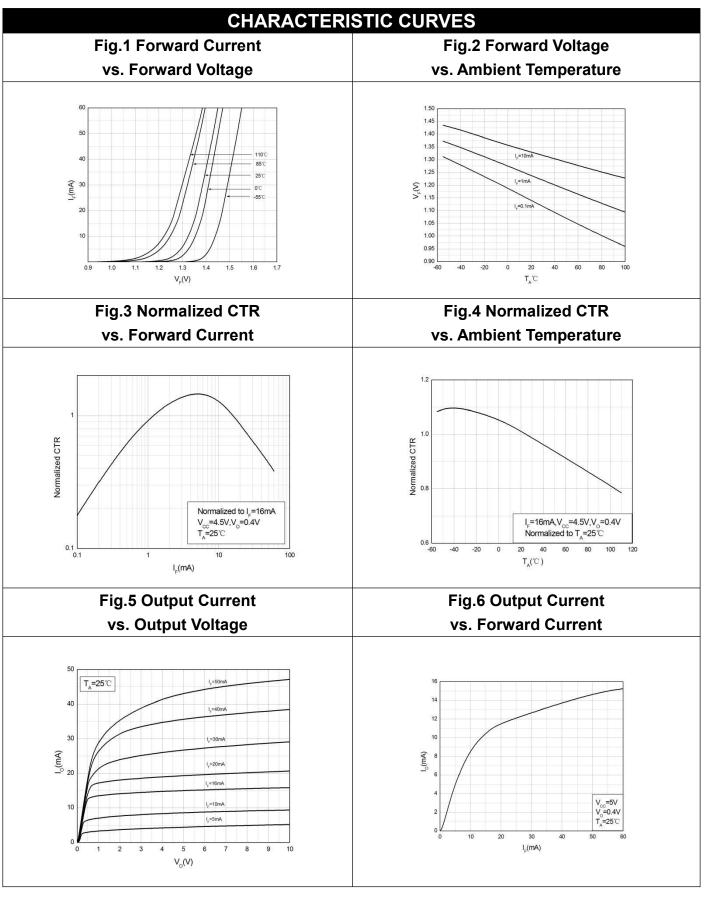
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ELECTRICAL OPTICAL CHARACTERISTICS							
PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION	NOTE
SWITCHING CHARACTERISTICS(at Ta=0 to 70°C, I _F =16mA ,V _{CC} =5V, unless specified otherwise)							
Propagation		_	0.4	0.8		$R_L=1.9k\Omega, T_A=25^{\circ}C$	
Delay Time	TPHL				μs	·	Fig.13
to Logic Low		-	-	1.0		R _L =1.9kΩ	
Propagation		_	0.35	0.8		$R_L=1.9k\Omega$, $T_A=25^{\circ}C$	
Delay Time	TPLH		0.00	0.0	μs	112,17, 20 0	Fig.13
to Logic High		-	-	1.0		$R_L=1.9k\Omega$	
Common Mode						L = 0m/L \/. =1500\/nn	
Transient Immunity	СМн	15	_	_	kV/µs	$I_F = 0 \text{mA}$, $V_{CM} = 1500 \text{Vpp}$,	Fig.15
at Logic High						RL=1.9kΩ, T _A =25°C	
Common Mode						L = 16mA \/CM=1500\/nn	
Transient Immunity	CML	15	_	_	kV/µs	I_F = 16mA , VCM=1500Vpp, R_L =1.9kΩ, T_A =25°C	Fig.15
at Logic Low						KL-1.9K12, 1A =25 C	



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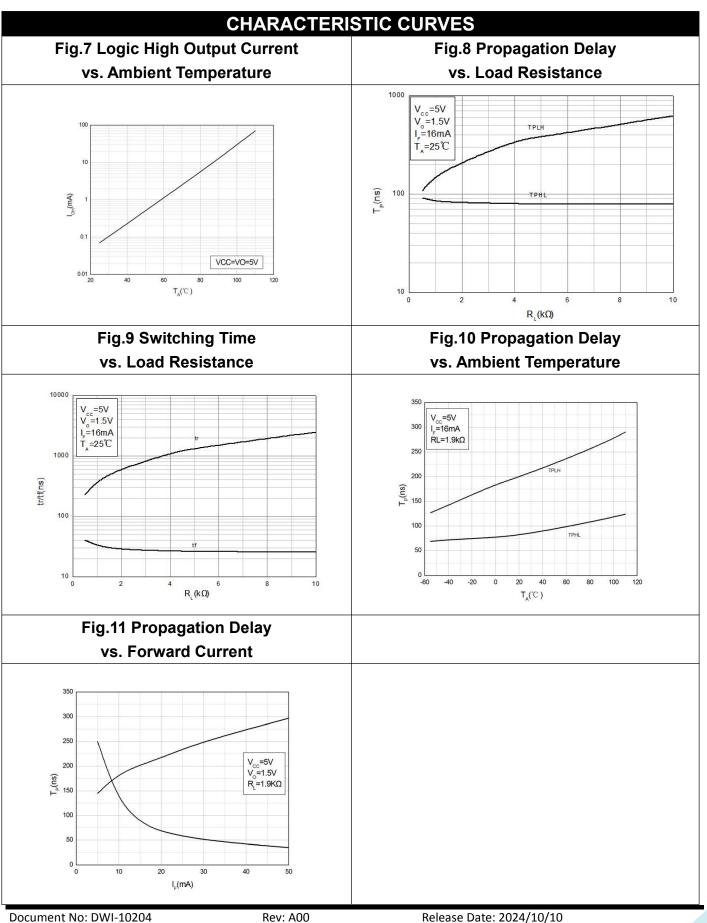
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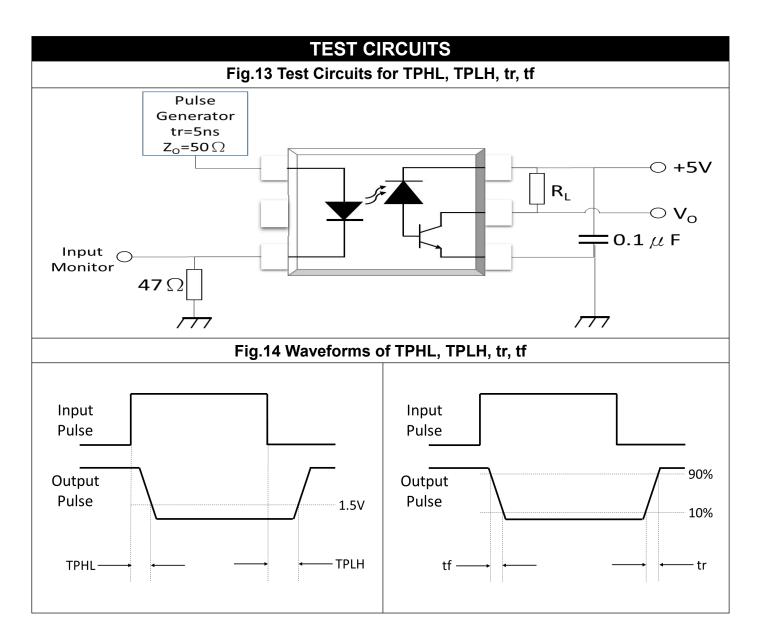
Rev: A00

Release Date: 2024/10/10

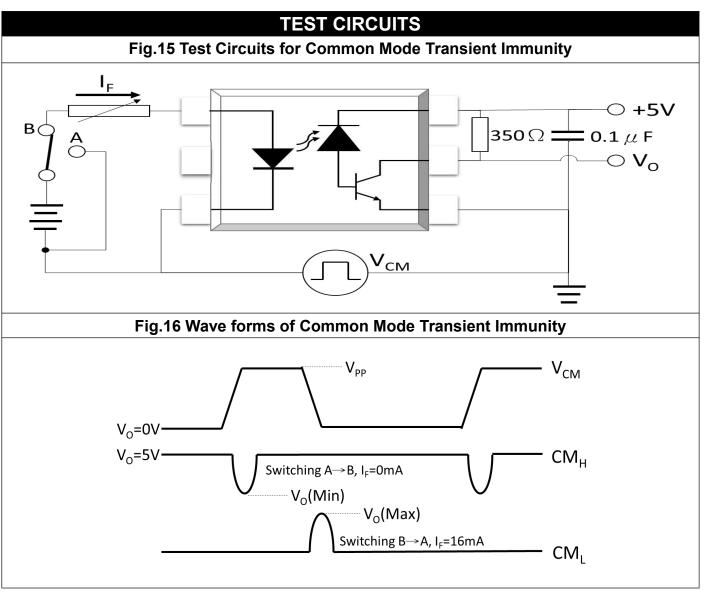








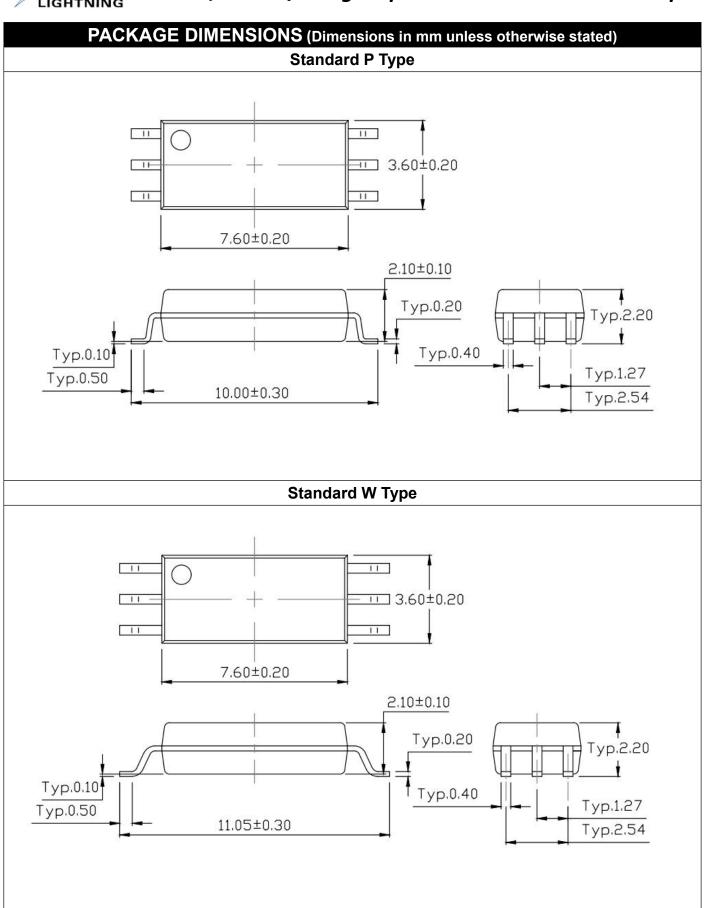




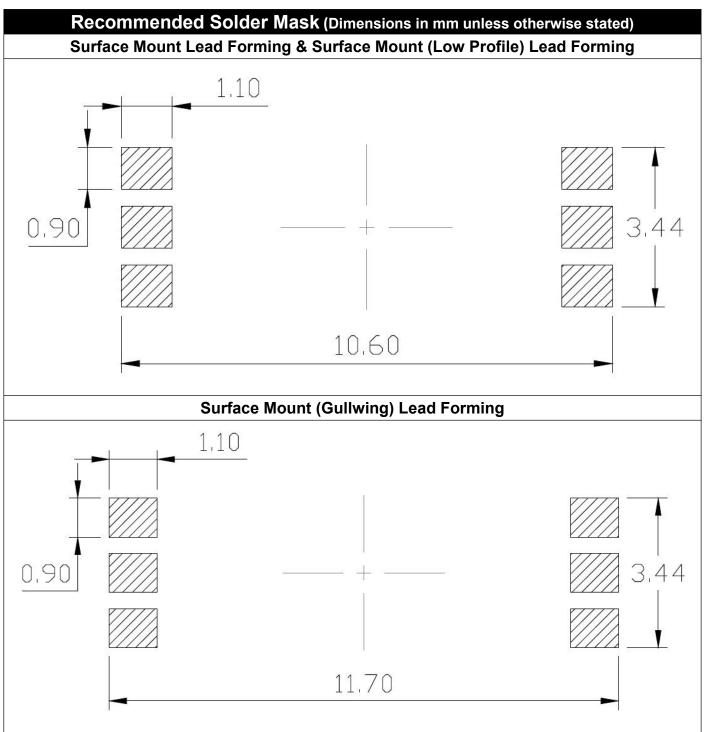


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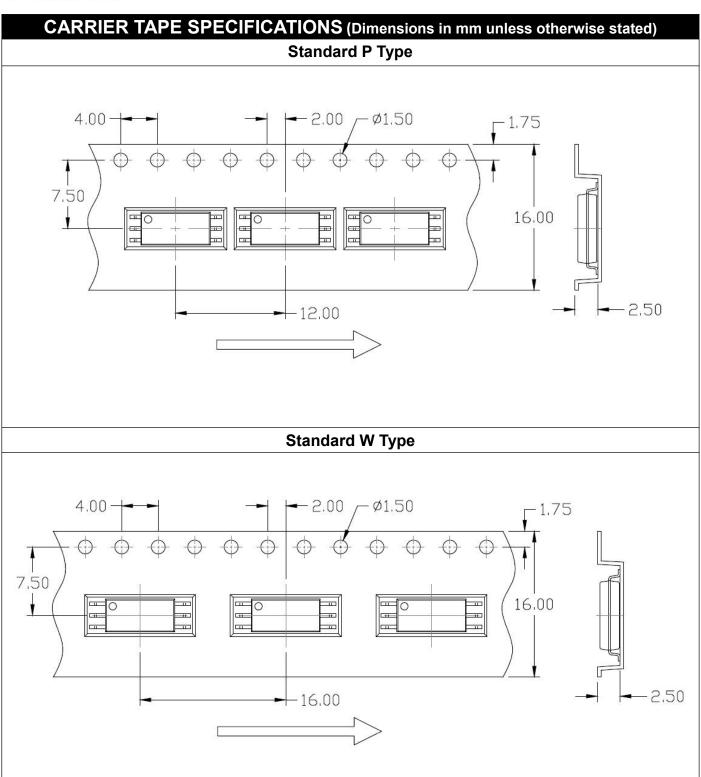
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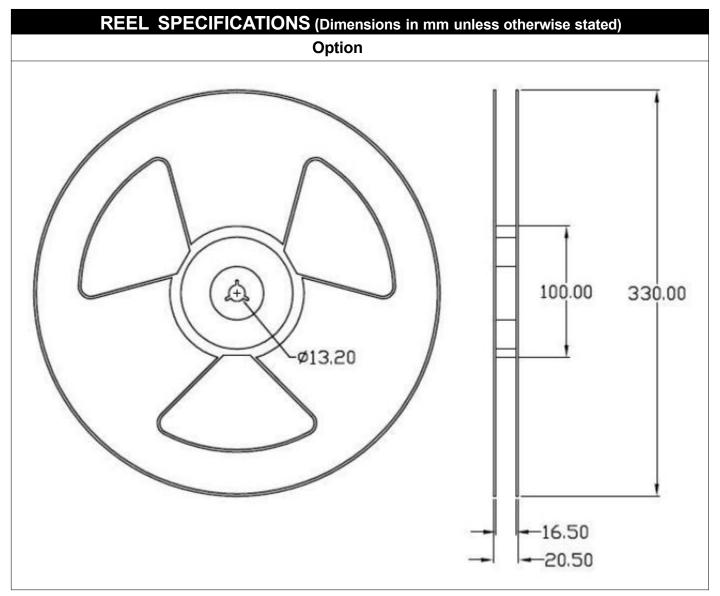




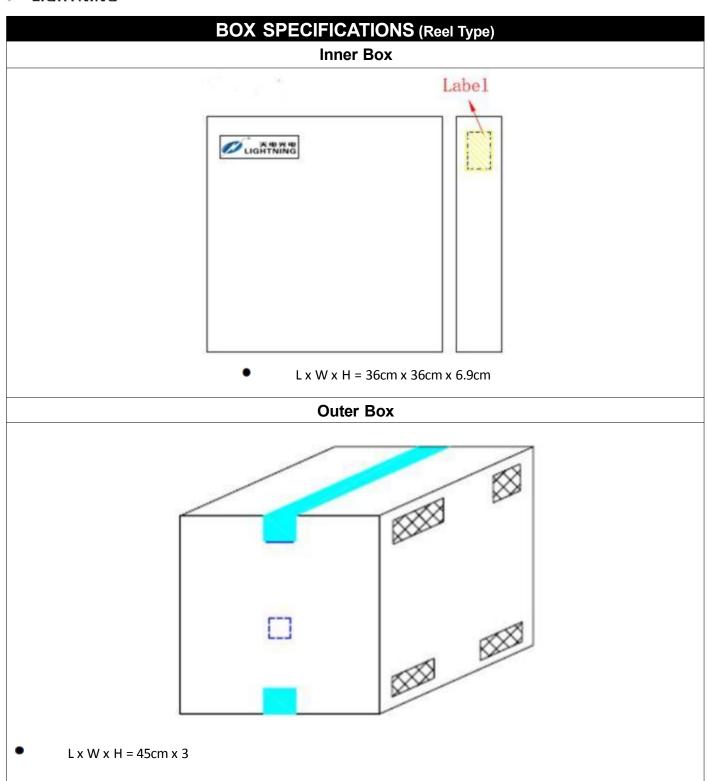














ORDERING AND MARKING INFORMATION

MARKING INFORMATION



TD: Company Abbr.

501 : Part Number

V : VDE Option

Y : Fiscal Year

A : Manufacturing Code

WW : Work Week

ORDERING INFORMATION

TDL501(Y)(Z)-GV

TD – Company Abbr

L-LSOP6

501 - Part Number

Y – Lead Form Option (P/W)

Z – Tape and Reel Option (T3)

G – Material Option

(G: Green, None: Non-Green)

V – VDE Option (V or None)



PACKING QUANTITY

Option	Quantity	Quantity – Inner box	Quantity – Outer box			
Т3	1500 Units/Reel	3 Reels/Inner box	5 Inner box/Outer box = 22.5k Units			



REFLOW INFORMATION REFLOW PROFILE Supplier T_p ≥ T_c User T_p ≤ T_c T_C -5°C Supplier tp T_p Temperature 📑 -T_c -5°C Max. Ramp Up Rate = 3°C/s Max. Ramp Down Rate = 6°C/s T_L T_{smax} Preheat Area T_{smin} 25 Time 25°C to Peak Time ⇒ IPC-020d-5-1

Profile Feature	Sn-Pb Assembly Profile	Pb-Free Assembly Profile
Temperature Min. (Tsmin)	100	150°C
Temperature Max. (Tsmax)	150	200°C
Time (ts) from (Tsmin to Tsmax)	60-120 seconds	60-120 seconds
Ramp-up Rate (tL to tP)	3°C/second max.	3°C/second max.
Liquidous Temperature (TL)	183°C	217°C
Time (tL) Maintained Above (TL)	60 – 150 seconds	60 – 150 seconds
Peak Body Package Temperature	235°C +0°C / -5°C	260°C +0°C / -5°C
Time (tP) within 5°C of 260°C	20 seconds	30 seconds
Ramp-down Rate (TP to TL)	6°C/second max	6°C/second max
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.



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- Please contact LIGHTNING sales agent for special application request.
- Immerge unit's body in solder paste is not recommended.
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